

W541L240 Data Sheet



4-BIT MICROCONTROLLER

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1. GENERAL DESCRIPTION

The W541L240 is fully compatible with W741L240 in the terms of pin assignment and IC function except main oscillator. It is a high-performance 4-bit microcontroller (μ C) that provides an LCD driver. The device contains a 4-bit ALU, a 8-bit timer, a divider, a 24×4 LCD driver, and three 4-bit I/O ports (including 1 output port for LED driving). There are also three interrupt sources and 8-level subroutine nesting for interrupt applications. The W541L240 operates on low voltage and very low current and has two power reduction modes, hold mode and stop mode, which help to minimize power dissipation.

The W541L240 is suitable for remote controllers, watches and clocks, multiple I/O products, keyboard controllers, speech synthesis LSI controllers, and other products.

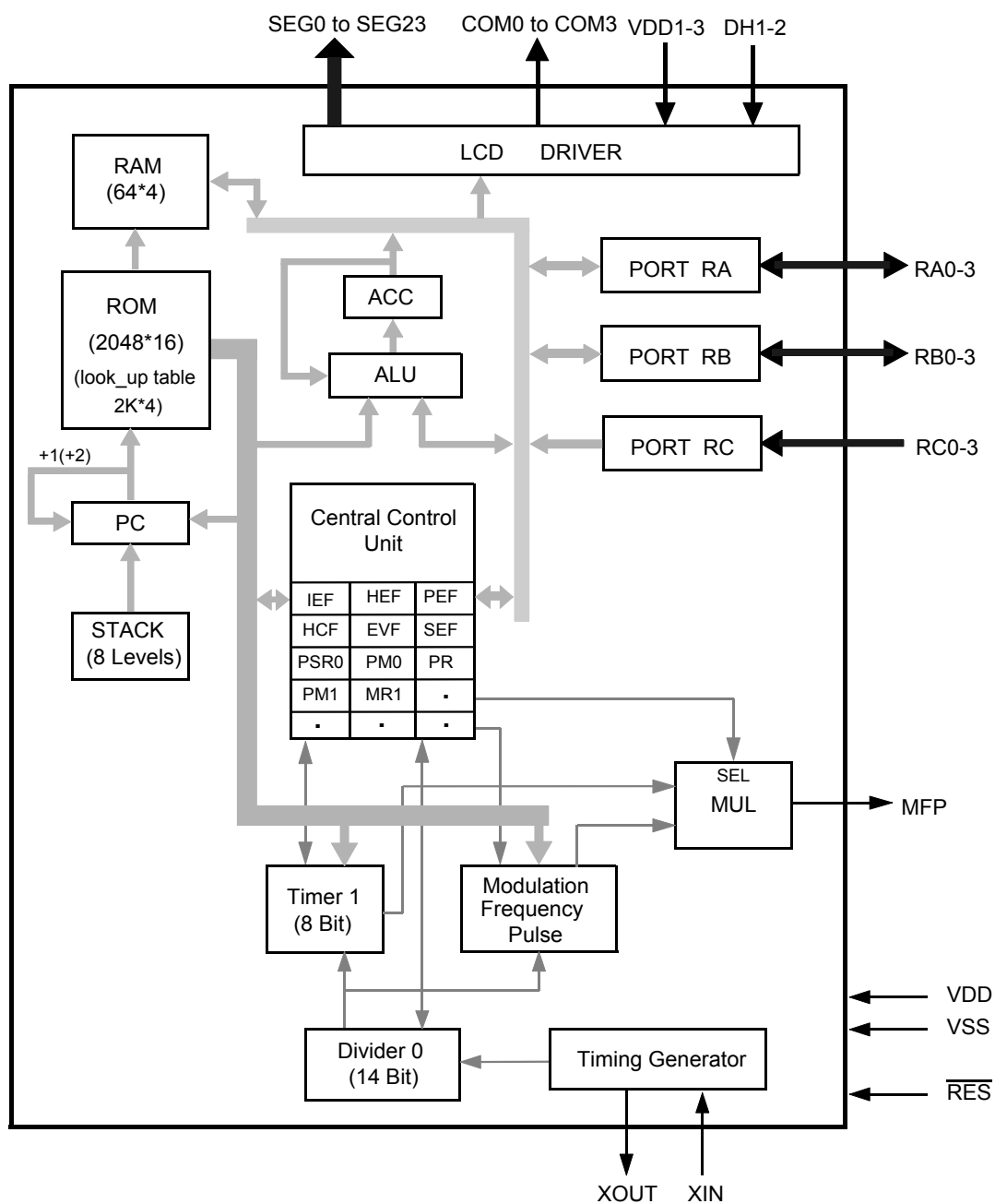
2. FEATURES

- Operating voltage: 1.2 ~ 1.8V (LCD drive voltage: 3.0V, or 4.5V)
- Crystal/RC oscillation circuit selectable by code option for system clock
- Crystal oscillator: 32.768 KHz only
- RC oscillator: 1 MHz (maximum)
 - High-frequency (400 KHz to 1 MHz) or low-frequency (below 400 KHz) oscillation option must be determined by the code option.
 - In RC mode, attention must be paid to the high/low frequency oscillation option, because the LCD driver frequency is related to this option.
- Memory
 - 2048 \times 16 bit program ROM (including 2K \times 4 bit look-up table)
 - 64 \times 4 bit data RAM (including 16 working registers)
 - 24 \times 4 LCD data RAM
- 13 input/output pins
 - Ports for input only: 1 port/4 pins
 - Input/output ports: 2 ports/8 pins
 - MFP output pin: 1 pin (MFP)
 - Do not be floating when it is as input or output open-drain (NMOS type).
- Power-down mode
 - Hold function: no operation (except for oscillator)
 - Stop function: no operation (including main clock)
- Three types of interrupts
 - Two internal interrupts (Divider 0, Timer 1)
 - One external interrupts (Port RC)

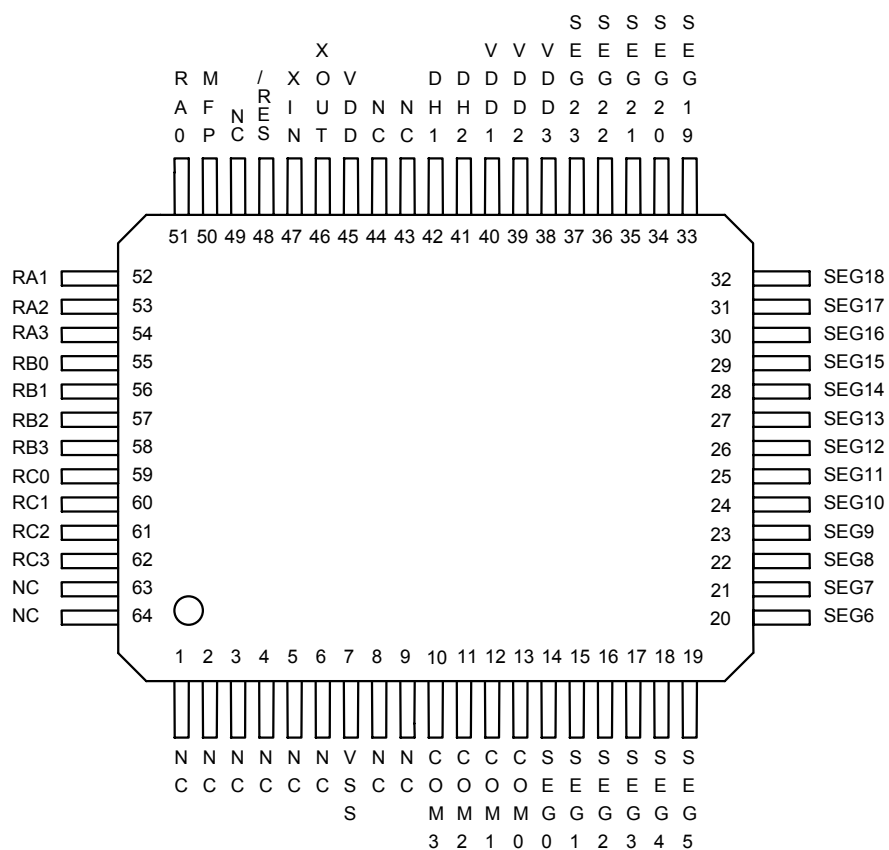


- LCD driver output
 - 24 segment \times 4 common
 - Static, 1/2 duty (1/2 bias), 1/3 duty (1/2 or 1/3 bias), 1/4 duty (1/3 bias) driving mode can be selected
- MFP output pin
 - Output is software selectable as modulating or nonmodulating frequency
 - Works as frequency output specified by Timer 1
- Built-in 14-bit clock frequency divider circuit
- One built-in 8-bit programmable countdown timers
 - Timer 1: Offers auto-reload function and one of two internal clock frequencies (F_{OSC} or $F_{OSC}/64$) can be selected (output through MFP pin)
- Built-in 18/14-bit watchdog timer selectable for system reset
- Powerful instruction set: 100 instructions
- 8-level subroutine (include interrupt) nesting
- Up to 4 μ S instruction cycle (with 1 MHz operating frequency)
- Packaged in 64-pin QFP

3. BLOCK DIAGRAM



4. PIN CONFIGURATION



5. PIN DESCRIPTION

| SYMBOL | I/O | FUNCTION | | | | | | | | | | | | | | | | | | | | | | | | | |
|------------------|----------|--|----------|----------|----------|----------|----------|------|------|------|------|------|------|----------|------|------|------|------|----------|----------|------|------|------|----------|----------|----------|------|
| XIN | I | Input pin for oscillator. Connected to crystal or resistor to generate system clock by code option. External 10~20pF capacitor uses to get accurate freq in crystal mode. | | | | | | | | | | | | | | | | | | | | | | | | | |
| XOUT | O | Output pin for oscillator. Connected to crystal or resistor to generate system clock by code option. 20pF capacitor is built in internal for crystal mode... | | | | | | | | | | | | | | | | | | | | | | | | | |
| RA0–RA3 | I/O | Input/Output port. Input/output mode specified by port mode 1 register (PM1). | | | | | | | | | | | | | | | | | | | | | | | | | |
| RB0–RB3 | I/O | Input/Output port. Input/output mode specified by port mode 2 register (PM2). | | | | | | | | | | | | | | | | | | | | | | | | | |
| RC0–RC3 | I | 4-bit port for input only. Each pin has an independent interrupt capability. And build-in schmitt trigger. | | | | | | | | | | | | | | | | | | | | | | | | | |
| MFP | O | Output pin only. This pin can output modulating or nonmodulating frequency, or Timer 1 clock output specified by mode register 1 (MR1). | | | | | | | | | | | | | | | | | | | | | | | | | |
| RES | I | System reset pin with pull-high resistor. | | | | | | | | | | | | | | | | | | | | | | | | | |
| SEG0–SEG23 | O | LCD segment output pins. Also can be used as DC output ports specified by code option. | | | | | | | | | | | | | | | | | | | | | | | | | |
| COM0–COM3 | O | LCD common signal output pins. <table border="1"><thead><tr><th></th><th>Static</th><th>1/2 Duty</th><th>1/3 Duty</th><th>1/4 Duty</th></tr></thead><tbody><tr><td>COM0</td><td>Used</td><td>Used</td><td>Used</td><td>Used</td></tr><tr><td>COM1</td><td>Not Used</td><td>Used</td><td>Used</td><td>Used</td></tr><tr><td>COM2</td><td>Not Used</td><td>Not Used</td><td>Used</td><td>Used</td></tr><tr><td>COM3</td><td>Not Used</td><td>Not Used</td><td>Not Used</td><td>Used</td></tr></tbody></table> <p>The LCD alternating frequency can be selected by code option.</p> | | Static | 1/2 Duty | 1/3 Duty | 1/4 Duty | COM0 | Used | Used | Used | Used | COM1 | Not Used | Used | Used | Used | COM2 | Not Used | Not Used | Used | Used | COM3 | Not Used | Not Used | Not Used | Used |
| | Static | 1/2 Duty | 1/3 Duty | 1/4 Duty | | | | | | | | | | | | | | | | | | | | | | | |
| COM0 | Used | Used | Used | Used | | | | | | | | | | | | | | | | | | | | | | | |
| COM1 | Not Used | Used | Used | Used | | | | | | | | | | | | | | | | | | | | | | | |
| COM2 | Not Used | Not Used | Used | Used | | | | | | | | | | | | | | | | | | | | | | | |
| COM3 | Not Used | Not Used | Not Used | Used | | | | | | | | | | | | | | | | | | | | | | | |
| DH1, DH2 | I | Connection terminals for voltage doubler (halver) capacitor. | | | | | | | | | | | | | | | | | | | | | | | | | |
| VDD1, VDD2, VDD3 | I | Positive (+) supply voltage terminal. Refer to Functional Description. | | | | | | | | | | | | | | | | | | | | | | | | | |
| VDD | I | Positive power supply (+). | | | | | | | | | | | | | | | | | | | | | | | | | |
| VSS | I | Negative power supply (-). | | | | | | | | | | | | | | | | | | | | | | | | | |



6. ELECTRICAL CHARACTERISTICS

6.1 Absolute Maximum Ratings

| PARAMETER | RATING | UNIT |
|------------------------------------|--------------|------|
| Supply Voltage to Ground Potential | -0.3 to +7.0 | V |
| Applied Input/Output Voltage | -0.3 to +7.0 | V |
| Power Dissipation | 120 | mW |
| Ambient Operating Temperature | 0 to +70 | °C |
| Storage Temperature | -55 to +150 | °C |

Note: Exposure to conditions beyond those listed under Absolute Maximum Ratings may adversely affect the life and reliability of the device.

6.2 DC Characteristics

(V_{DD}-V_{SS} = 1.5V, F_{OSC} = 32.768 KHz, T_A = 25° C; unless otherwise specified)

| PARAMETER | SYM. | CONDITIONS | MIN. | TYP. | MAX. | UNIT |
|--------------------------------|------------------|------------------------------|---------------------|------|---------------------|------|
| Op. Voltage | V _{DD} | - | 1.2 | - | 1.8 | V |
| Op. Current (Crystal type) | I _{OP1} | No load (Ext-V) | - | 4 | 12 | μA |
| Op. Current (RC type) | I _{OP2} | No load (Ext-V) | - | 35 | 65 | μA |
| Hold Current (Crystal type) | I _{HM1} | Hold mode No load (Ext-V) | - | 3 | 6 | μA |
| Hold Current (RC type) | I _{HM2} | Hold mode No load (Ext-V) | - | 16 | 40 | μA |
| Stop Current (Crystal type) | I _{SM1} | Stop mode No load (Ext-V) | - | 0.1 | 2 | μA |
| Stop Current (RC type) | I _{SM2} | Stop mode No load (Ext-V) | - | 0.1 | 2 | μA |
| Input Low Voltage | V _{IL} | - | V _{SS} | - | 0.3 V _{DD} | V |
| Input High Voltage | V _{IH} | - | 0.7 V _{DD} | - | V _{DD} | V |
| MFP Output Low Voltage | V _{ML} | I _{OL} = 0.9mA | - | - | 0.3 | V |
| MFP Output High Voltage | V _{MH} | I _{OH} = -0.75mA | 1.2 | - | - | V |
| Port RA, RB Output Low Voltage | V _{ABL} | I _{OL} = 1.0mA | - | 0.2 | 0.3 | V |
| Port RA, RB Output Hi Voltage | V _{ABH} | I _{OH} = -0.5mA | 1.2 | 1.4 | - | V |
| LCD Supply Current | I _{LCD} | All Seg. On | - | - | 3 | μA |



DC Characteristics, continued

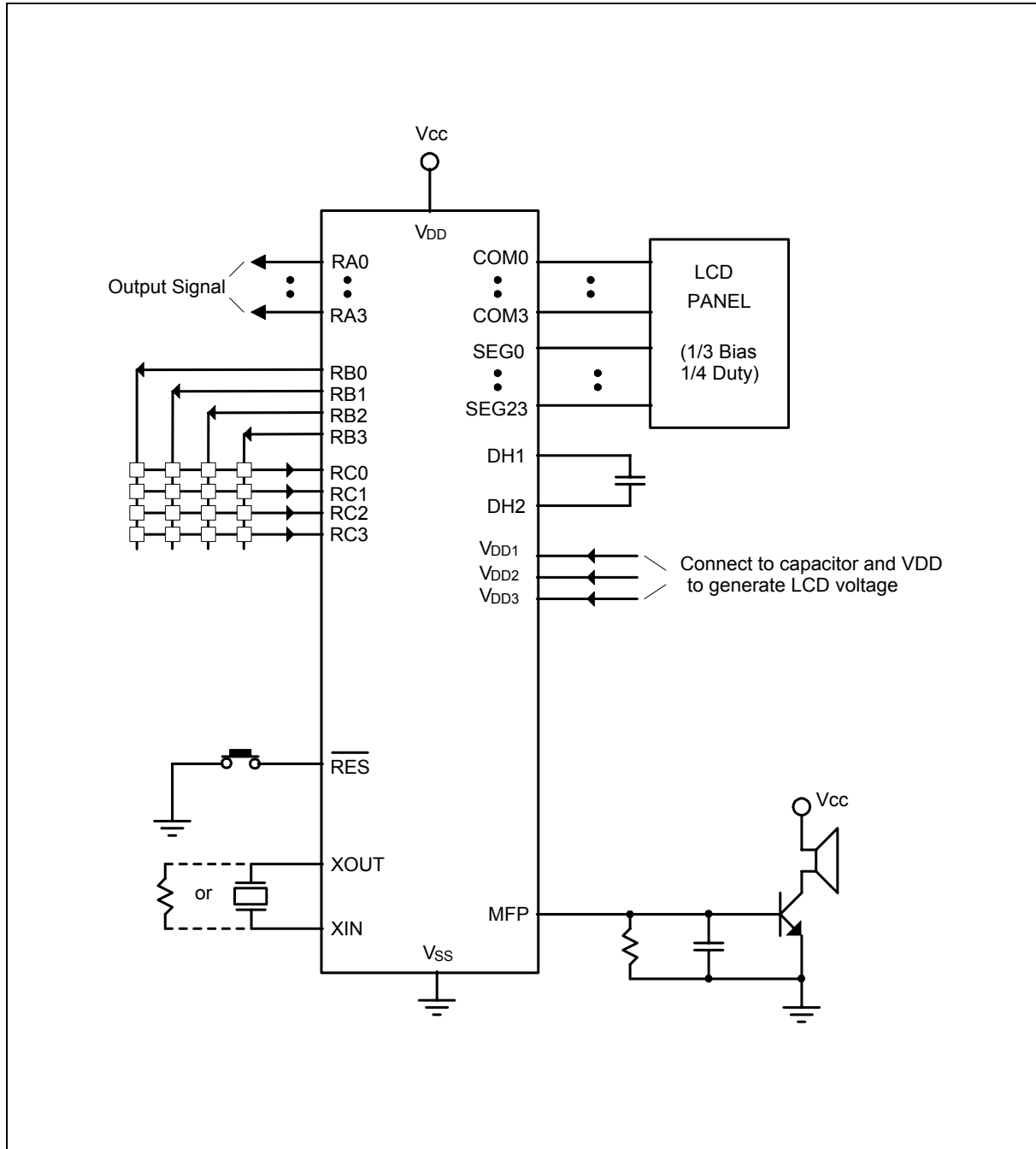
| PARAMETER | SYM. | CONDITIONS | MIN. | TYP. | MAX. | UNIT |
|--|------|----------------------------|------|------|------|------|
| SEG0–SEG23 Sink Current | IOL | VOL = 0.05V VLCD = 0.0V | 6 | - | - | μA |
| SEG0–SEG23 Drive Current | IOH | VOH = 4.45V VLCD = 4.5V | 1.5 | - | - | μA |
| Input Port Pull-up Resistor | RC | Port RC | 200 | 1000 | 1500 | KΩ |
| $\overline{\text{RES}}$ Pull-up Resistor | RRES | - | 200 | 500 | 1500 | KΩ |

6.3 AC Characteristics

(VDD–VSS = 1.5V, TA = 25° C; unless otherwise specified)

| PARAMETER | SYM. | CONDITIONS | MIN. | TYP. | MAX. | UNIT |
|--------------------------|------|---|------|--------|------|------|
| Op. Frequency | FOSC | RC type | - | - | 1000 | KHz |
| | | Crystal type (Option low-speed type only) | - | 32.768 | - | |
| Oscillator Start-up Time | TS | VDD = 1.2 V, FOSC = 32.768 KHz | - | 1 | 2 | S |
| Instruction Cycle Time | TI | One machine cycle | - | 4/FOSC | - | mS |
| Reset Active Width | TRAW | FOSC = 32.768 KHz | 1 | - | - | μS |
| Interrupt Active Width | TIAW | FOSC = 32.768 KHz | 1 | - | - | μS |

7. APPLICATION CIRCUIT





8. REVISION HISTORY

| VERSION | DATE | DESCRIPTION |
|---------|--------------|---|
| A2 | - | Note: Recommend working voltage range for Hi freq X all mode (L series) |
| A3 | - | Note: RC is built-in schmitt trigger |
| A4 | - | Note: Do not be floating when it is as input or output open-drain (NMOS type) |
| A5 | May 29, 2003 | Note : Update DC psec formate |



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