

# Medium power Transistor(−32V, −2A)

## 2SB1188/2SB1182/2SB1240/2SB891F/ 2SB822/2SB1277/2SB911M

### ●Features

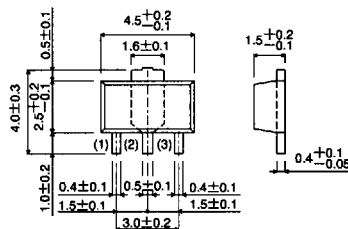
- 1) Low  $V_{CE(sat)}$ .  
 $V_{CE(sat)} = -0.5V$  (Typ.)  
( $I_C/I_B = -2A/-0.2A$ )
- 2) Complements the 2SD1766/  
2SD1758/2SD1862/2SD1189F/  
2SD1055/2SD19192/SD1227M.

### ●Structure

Epitaxial planar type  
PNP silicon transistor

### ●External dimensions (Units: mm)

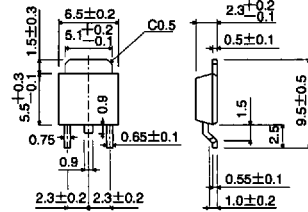
2SB1188



(1) Base  
ROHM : MPT3  
EIAJ : SC-62  
(2) Collector  
(3) Emitter

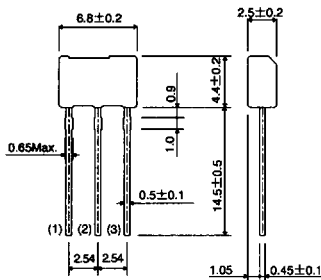
Abbreviated symbol: BC \*

2SB1182



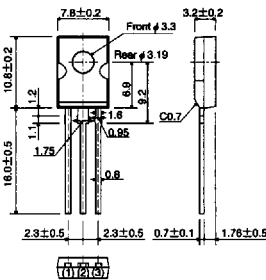
(1) Base  
ROHM : CPT3  
EIAJ : SC-63  
(2) Collector  
(3) Emitter

2SB1240



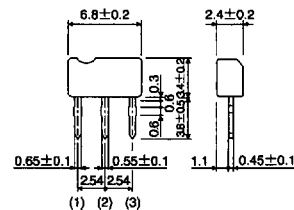
ROHM : ATV  
(1) Emitter  
(2) Collector  
(3) Base

2SB891F



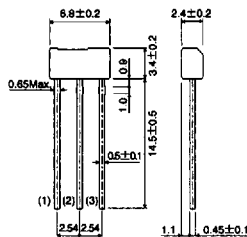
ROHM : TO-126FP  
(1) Emitter  
(2) Collector  
(3) Base

2SB822



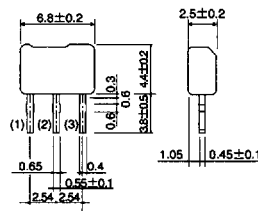
ROHM : FTR  
(1) Emitter  
(2) Collector  
(3) Base

2SB1277



ROHM : FTL  
(1) Emitter  
(2) Collector  
(3) Base

2SB911M



ROHM : ATR  
EIAJ : SC-71  
(1) Emitter  
(2) Collector  
(3) Base

\*Denotes hFE

● Absolute maximum ratings ( $T_a = 25^\circ\text{C}$ )

Parameter		Symbol	Limits	Unit
Collector-base voltage		$V_{CB0}$	-40	V
Collector-emitter voltage		$V_{CE0}$	-32	V
Emitter-base voltage		$V_{EB0}$	-5	V
Collector current		$I_c$	-2	A (DC)
			-3	A (Pulse) * 1
Collector power dissipation	2SB1188	$P_c$	0.5	W * 2
			2	
	2SB1182		10	$W(T_c=25^\circ\text{C})$
	2SB1240,2SB911M		1	W * 3
	2SB891F		1.2	
	2SB822,2SB1277		5	$W(T_c=25^\circ\text{C})$
Junction temperature		$T_j$	150	$^\circ\text{C}$
Storage temperature		$T_{stg}$	-55~150	$^\circ\text{C}$

\* 1 Single pulse  $P_w=100\text{ms}$ 

\* 2 On 40 x 40 x 0.7 mm ceramic board is used.

\* 3 Printed circuit board 1.7mm thick, collector copper plating  $1\text{cm}^2$  or larger.● Electrical characteristics ( $T_a = 25^\circ\text{C}$ )

Parameter		Symbol	Min.	Typ.	Max.	Unit	Conditions		
Collector-base breakdown voltage		$BV_{CBO}$	-40	—	—	V	$I_C=-50\mu A$		
Collector-emitter breakdown voltage		$BV_{CEO}$	-32	—	—	V	$I_C=-1mA$		
Emitter-base breakdown voltage		$BV_{EBO}$	-5	—	—	V	$I_E=-50\mu A$		
Collector cutoff current		$I_{CBO}$	—	—	-1	$\mu A$	$V_{CB}=-20V$		
Emitter cutoff current		$I_{EBO}$	—	—	-1	$\mu A$	$V_{EB}=-4V$		
Collector-emitter saturation voltage		$V_{CE(sat)}$	—	-0.5	-0.8	V	$I_C/I_E=-2A/-0.2A$ *		
DC current transfer ratio	2SB1188,2SB1182	$h_{FE}$	82	—	390	—	$V_{CE}=-3V, I_C=-0.5A$ *		
	2SB1240,2SB891F								
	2SB822,2SB1277		120	—	390				
	2SB891F								
Transition frequency		$f_T$	—	100	—	MHz	$V_{CE}=-5V, I_E=0.5A, f=30MHz$		
Output capacitance		$C_{ob}$	—	50	—	pF	$V_{CB}=-10V, I_E=0A, f=1MHz$		

\* Measured using pulse current.

## ● Packaging specifications and $h_{FE}$

Type	$h_{FE}$	Package	Taping				Bulk	
		Code	T100	TL	TU2	TL2	—	—
		Basic ordering unit (pieces)	1000	2500	2500	2500	1000	2000
2SB1188	PQR		○	—	—	—	—	—
2SB1182	PQR		—	○	—	—	—	—
2SB1240	QR		—	—	○	—	—	—
2SB891F	PQR		—	—	—	—	○	—
2SB822	Q		—	—	—	—	—	○
2SB1277	Q		—	—	—	○	—	—
2SB911M	Q		—	—	—	—	—	○

$h_{FE}$  values are classified as follows :

Item	P	Q	R
$h_{FE}$	82~180	120~270	180~390

## ● Electrical characteristic curves

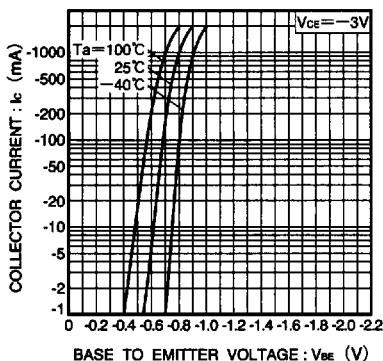


Fig.1 Grounded emitter propagation characteristics

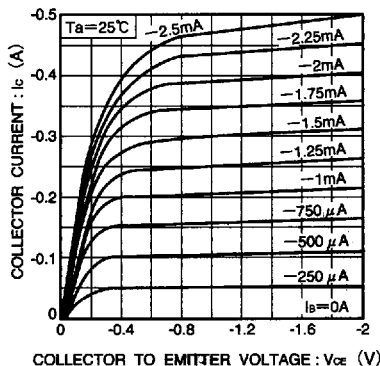


Fig.2 Grounded emitter output characteristics

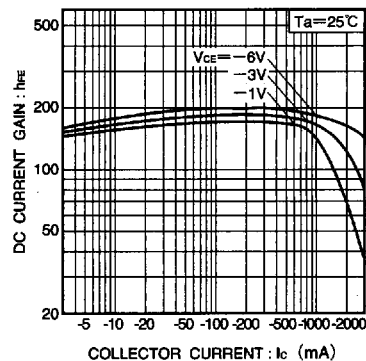


Fig.3 DC current gain vs. collector current ( $I_c$ )

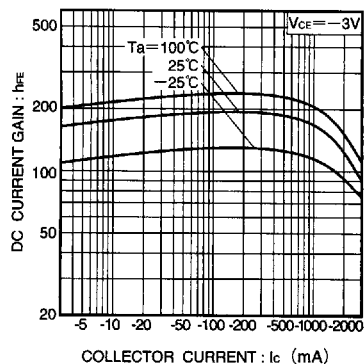


Fig.4 DC current gain vs. collector current (I)

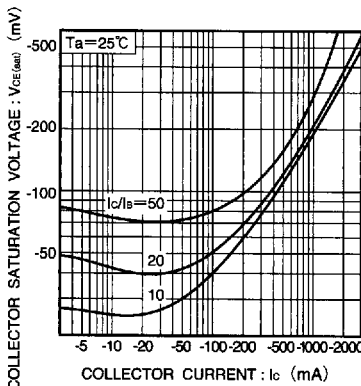


Fig.5 Collector-emitter saturation voltage vs. collector current (I)

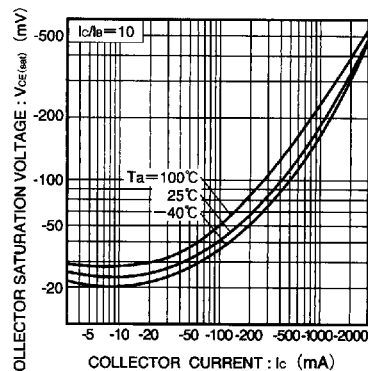


Fig.6 Collector-emitter saturation voltage vs. collector current (II)

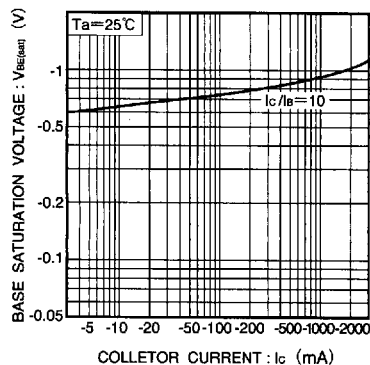


Fig.7 Base-emitter saturation voltage vs. collector current

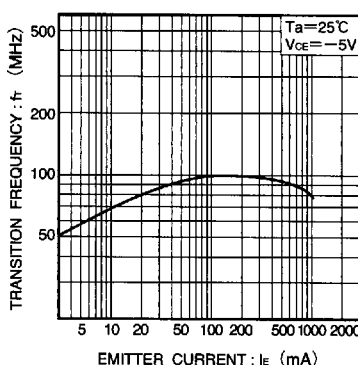


Fig.8 Gain bandwidth product vs. emitter current

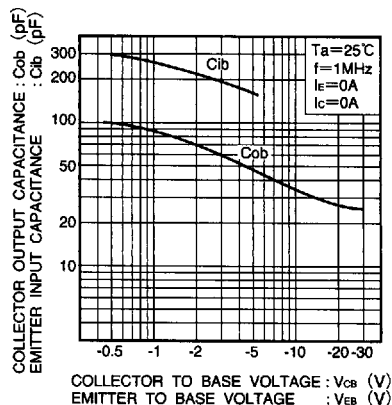


Fig.9 Collector output capacitance vs. collector-base voltage  
Emitter input capacitance vs. emitter-base voltage

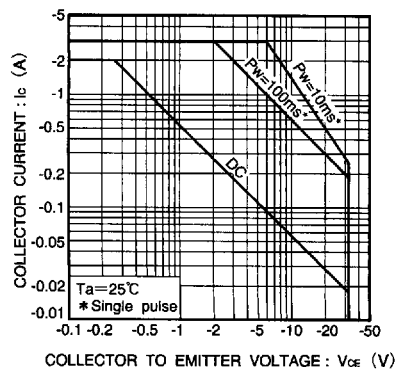


Fig.10 Safe operation area (2SB1188)

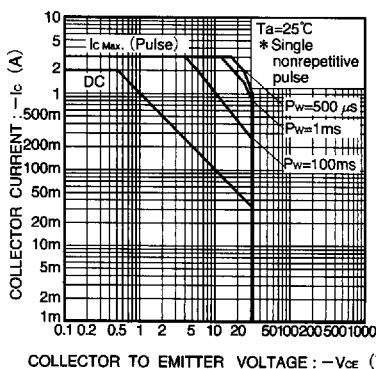


Fig.11 Safe operation area (2SB1182)

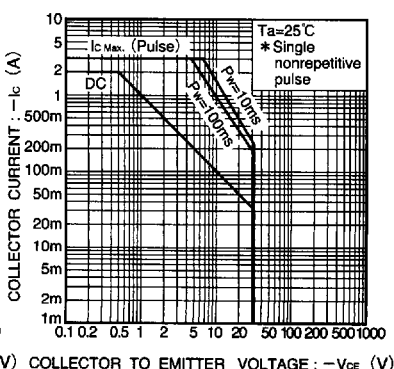


Fig.12 Safe operation area (2SB891)

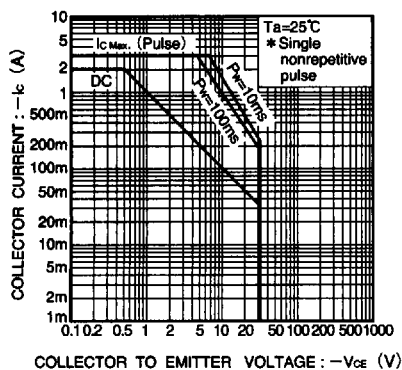
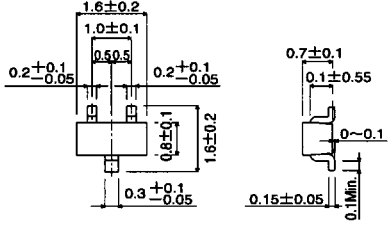
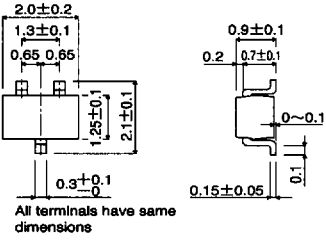
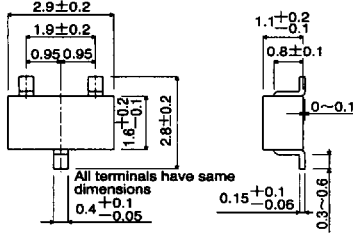
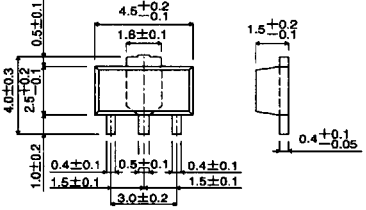


Fig.13 Safe operation area  
(2SB891 (TO-126M))

# Packages

ROHM has been manufacturing transistors since 1975. In the development of products, we constantly strive to anticipate the needs of our customers. Regarding packages, the demands of the market for compactness, low power consumption, low power dissipation and automatic mounting support are becoming ever greater, and we are strengthening our product development system to meet these needs.

## ●Types and features of surface-mount packages

Type	External dimensions (Units : mm)	Features
<b>EMT3</b> <b>SC-75A type</b>		<p>A more compact version of the UMT3 (SC-70), the EMT3 is the world's smallest transistor with a mold size of <math>1.6 \times 0.8</math> mm. The mounting area is approximately 60% of the UMT3 and 30% of the SMT3, making it ideal for ultra-high density mounting. Mounting is possible with the same type of automatic mounting machine as the UMT3.</p>
<b>UMT3</b> <b>SC-70 type</b>		<p>The UMT3 is a smaller version of the SMT3 (SC-59). The mounting area is approximately 60% of the SMT3, making it optimum for high density mounting. The taping size is the same as the SMT3, allowing use of conventional automatic mounting machines. Electrical characteristics and reliability are the same as the SMT3.</p>
<b>SMT3</b> <b>SC-59 type</b>		<p>The SMT3 is a compact package suitable for small electronic devices and hybrid IC applications. With proven performance, this is one of the most basic small packages. With the exception of <math>P_c</math> (collector power dissipation), electrical characteristics are similar to leaded packages. Reliability is on the same level as the TO-92.</p>
<b>MPT3</b> <b>SC-62 type</b>		<p>By itself the MPT3 has a <math>P_c</math> of 0.5 W (<math>T_a = 25^\circ\text{C}</math>), but when used on a <math>40 \times 40 \times 0.7</math> mm ceramic board, <math>P_c = 2</math> W (<math>T_c = 25^\circ\text{C}</math>), allowing high power to be obtained with a small package. The flat package makes it suitable for applications requiring compactness such as hybrid ICs. Available on tape for automatic mounting.</p>

Type	External dimensions (Units : mm)	Features
CPT 3 SC-63 type		<p>By itself the CPT3 has a <math>P_c</math> of 1 W (<math>T_a = 25^\circ\text{C}</math>), but a large <math>P_c</math> of several watts can be obtained with an appropriate mounting surface. At the same time the CPT3 is compact, making it suitable for high density mounting and hybrid ICs. Available on tape for automatic mounting.</p> <p>For vertical high density mounting, the leaded CPT (SC-64) type with the same mold size is also available.</p>
PSD3		<p>The PSD3 is a TO-220 class surface-mount package. A high <math>P_c</math> can be obtained with an appropriate mounting surface. Surface mounting allows a high vertical density, enabling the design of slim and compact devices. The PSD3 is available on tape for automatic mounting, and it helps improve mounting efficiency and reduce mounting cost.</p>
SMT5 SC-74A type	<p>All terminals have same dimensions</p>	<p>The SMT5 consists of two connected transistors or digital transistors in an SMT3 (SC-59) package. The mounting area can be reduced by 50% compared to the SMT3 and the internal circuitry is complete, making this package ideal for high density mounting at half the assembly cost.</p>
SMT6 SC-74 type		<p>The SMT6 consists of two independent transistors or two independent digital transistors in an SMT3 (SC-59) package. The mounting area and mounting cost can be reduced by 50% compared to the SMT3, and the two transistors are independent to allow free configuration of a high density circuit.</p>

EXPLANATION

Type	External dimensions (Units : mm)	Features
UMT5 SC-88A type	<p>2.0±0.2 1.3±0.1 0.65 0.65 0.2±0.1 0.05 1.25±0.1 2.1±0.1 0.15±0.05 0.9±0.1 0.7 0~0.1 0.1Min.</p> <p>All terminals have same dimensions</p>	<p>The UMT5 consists of two connected transistors or digital transistors in a UMT3 (SC-70) package. The mounting area can be reduced by 50% compared to the UMT3 and the internal circuitry is completed, making this package ideal for high density mounting at half the assembly cost.</p>
UMT6 SC-88 type	<p>2.0±0.2 1.3±0.1 0.65 0.65 0.2±0.1 0.05 1.25±0.1 2.1±0.1 0.15±0.05 0.9±0.1 0.7 0~0.1 0.1Min.</p> <p>All terminals have same dimensions</p>	<p>The UMT6 consists of two independent transistors or two independent digital transistors in a UMT (SC-70) package. The mounting area and mounting cost can be reduced by 50% compared to the UMT3, and the two transistors are independent to allow free configuration of a high density circuit.</p>

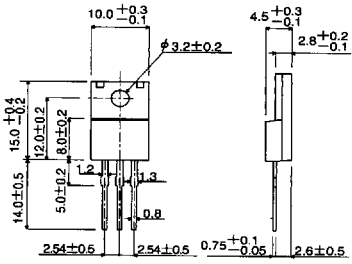


### ●Types and features of leaded packages

Type	External dimensions (Units : mm)	Features
SPT (SC-72 type)		<p>The SPT is a smaller version of the conventional TO-92 type. The body size (3×4×2 mm<sup>3</sup>) has been reduced to 1/4 that of the TO-92 (5×5×4 mm<sup>3</sup>). The SPT is available on tape for automatic insertion, and less space is occupied on the printed circuit board than the TO-92. Reliability is the same as the TO-92.</p>
FTR		<p>SIL type with a height of 3.4 mm and a lead pitch of 2.54 mm.</p>
FTL		<p>The FTL is a radial taping version of the highly popular FTR. This enables automatic high-density mounting with a radial insertion machine.</p>
ATR (SC-71 type)		<p>SC-71type with a height of 4.4 mm and a Pc=1W type.</p>

**EXPLANATION**

[illegible]

Type	External dimensions (Units : mm)	Features
TO-220FN	 <p>Technical drawing of the TO-220FN package showing top and side views with dimensions in mm:</p> <ul style="list-style-type: none"><li>Top view dimensions: 10.0<sup>+0.3</sup>/<sub>-0.1</sub> (width), 14.0<sup>+0.4</sup>/<sub>-0.2</sub> (height), 12.0<sup>+0.2</sup>/<sub>-0.2</sub> (height), 8.0<sup>+0.2</sup>/<sub>-0.2</sub> (height), 5.0<sup>+0.2</sup>/<sub>-0.2</sub> (height), 2.54<sup>+0.5</sup>/<sub>-0.5</sub> (width), 2.54<sup>+0.5</sup>/<sub>-0.5</sub> (width), 0.75<sup>+0.1</sup>/<sub>-0.05</sub> (width), 2.6<sup>+0.5</sup>/<sub>-0.5</sub> (width), 1.2 (width), 1.3 (width), 0.8 (width), <math>\phi 3.2 \pm 0.2</math> (hole diameter).</li><li>Side view dimensions: 4.5<sup>+0.3</sup>/<sub>-0.1</sub> (height), 2.8<sup>+0.2</sup>/<sub>-0.1</sub> (height), 2.6<sup>+0.5</sup>/<sub>-0.5</sub> (width).</li></ul>	<p>The TO-220FN features the same performance as the TO-220FP with approximately 2 mm less height, allowing the design of slimmer devices. Furthermore, the elimination of support pins in the fin (collector electrode) solves short-circuiting problems with neighboring components and the chassis.</p> <p>To make the height to the installation hole the same as the TO-220FP, it can be replaced as is from the TO-220FP.</p>

EXPLANATION