

### DESCRIPTION

The M63820FP/KP 8-channel sinkdriver, consists of 16 NPN transistors connected to from eight high current gain driver pairs.

### FEATURES

- High breakdown voltage ( $BV_{CEO} \geq 50V$ )
- High-current driving ( $I_{C(max)} = 500mA$ )
- With clamping diodes
- 3V micro computer series compatible input
- Wide operating temperature range ( $T_a = -40$  to  $+85^\circ C$ )

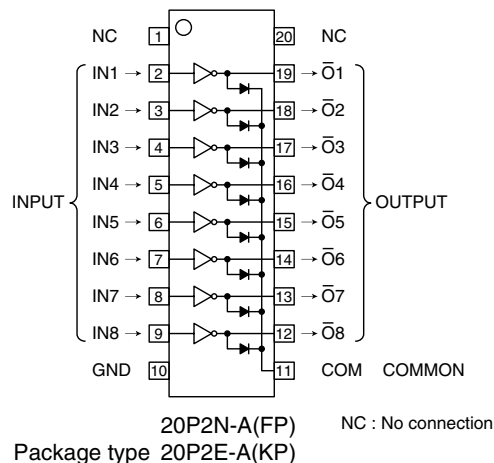
### APPLICATION

Output for 3 voltage microcomputer series and interface with high voltage system. Relay and small printer driver, LED, or incandescent display digit driver.

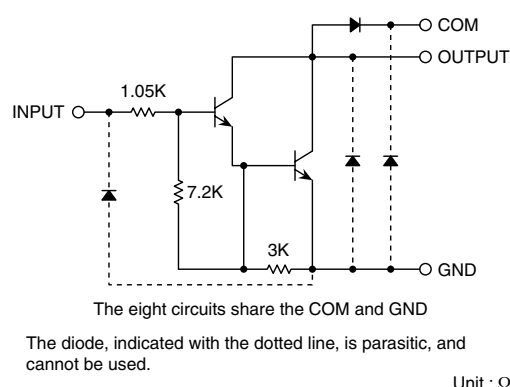
### FUNCTION

The M63820FP/KP is transistor-array of high active level eight units type which can do direct drive of 3 voltage micro-computer series. A resistor of  $1.05k\Omega$  is connected between the input pin. A clamp diode for inductive load transient suppression is connected for the output pin (collector) and COM pin (pin11). All emitters of the output transistor are connected to GND (pin10). The outputs are capable of driving 500mA and are rated for operation with output voltage up to 50V.

### PIN CONFIGURATION



### CIRCUIT DIAGRAM



### ABSOLUTE MAXIMUM RATINGS (Unless otherwise noted, $T_a = -40 \sim +85^\circ C$ )

Symbol	Parameter	Conditions	Ratings	Unit
$V_{CEO}$	Collector-emitter voltage	Output, H	$-0.5 \sim +50$	V
$I_C$	Collector current	Current per circuit output, L	500	mA
$V_I$	Input voltage		$-0.5 \sim +10$	V
$I_F$	Clamping diode forward current		500	mA
$V_R$	Clamping diode reverse voltage		50	V
$P_d$	Power dissipation	$T_a = 25^\circ C$ , when mounted on board	$1.10(GP)/0.68(KP)$	W
$T_{opr}$	Operating temperature		$-40 \sim +85$	$^\circ C$
$T_{stg}$	Storage temperature		$-55 \sim +125$	$^\circ C$

### RECOMMENDED OPERATING CONDITIONS (Unless otherwise noted, Ta = -40 ~ +85°C)

Symbol	Parameter		Limits			Unit
			min	typ	max	
Vo	Output voltage		0	—	50	V
Ic	Collector current (Current per 1 circuit when 8 circuits are coming on simultaneously)	Duty Cycle FP : no more than 4% KP : no more than 2%	0	—	400	mA
		Duty Cycle FP : no more than 15% KP : no more than 6%	0	—	200	
VIH	“H” input voltage	Ic ≤ 400mA	2.7	—	10	V
VIL	“L” input voltage		0	—	0.6	V

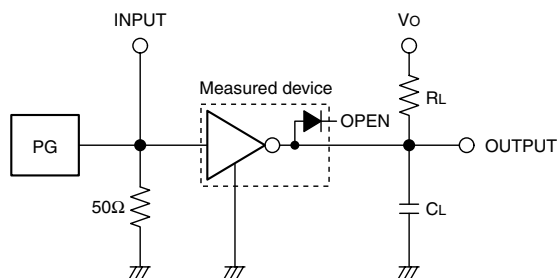
### ELECTRICAL CHARACTERISTICS (Unless otherwise noted, Ta = 25°C)

Symbol	Parameter	Test conditions	Limits			Unit
			min	typ	max	
V (BR) CEO	Collector-emitter breakdown voltage	I <sub>CEO</sub> = 100μA	50	—	—	V
V <sub>CE(sat)</sub>	Collector-emitter saturation voltage	I <sub>I</sub> = 500μA, I <sub>C</sub> = 350mA	—	1.2	1.6	V
		I <sub>I</sub> = 350μA, I <sub>C</sub> = 200mA	—	1.0	1.3	
		I <sub>I</sub> = 250μA, I <sub>C</sub> = 100mA	—	0.9	1.1	
I <sub>I</sub>	Input current	V <sub>I</sub> = 3V	—	1.5	2.4	mA
V <sub>F</sub>	Clamping diode forward voltage	I <sub>F</sub> = 350mA	—	1.4	2.0	V
I <sub>R</sub>	Clamping diode reverse current	V <sub>R</sub> = 50V	—	—	100	μA
h <sub>FE</sub>	DC amplification factor	V <sub>CE</sub> = 2V, I <sub>C</sub> = 350mA	1000	2500	—	—

### SWITCHING CHARACTERISTICS (Unless otherwise noted, Ta = 25°C)

Symbol	Parameter	Test conditions	Limits			Unit
			min	typ	max	
t <sub>on</sub>	Turn-on time	C <sub>L</sub> = 15pF (note 1)	—	15	—	ns
t <sub>off</sub>	Turn-off time		—	350	—	ns

#### NOTE 1 TEST CIRCUIT



- (1) Pulse generator (PG) characteristics : PRR=1kHz,  
t<sub>w</sub> = 10μs, t<sub>r</sub> = 6ns, t<sub>f</sub> = 6ns, Z<sub>o</sub> = 50Ω  
V<sub>I</sub> = 0 ~ 3V
- (2) Input-output conditions : R<sub>L</sub> = 25Ω, V<sub>O</sub> = 10V
- (3) Electrostatic capacity C<sub>L</sub> includes floating capacitance  
at connections and input capacitance at probes

#### TIMING DIAGRAM

