

**PNP Epitaxial Planar Transistor**

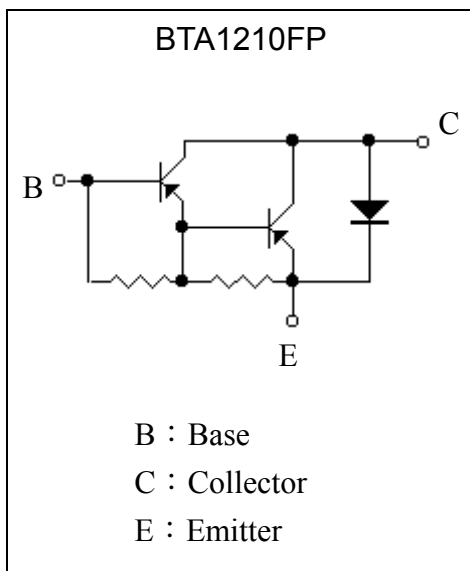
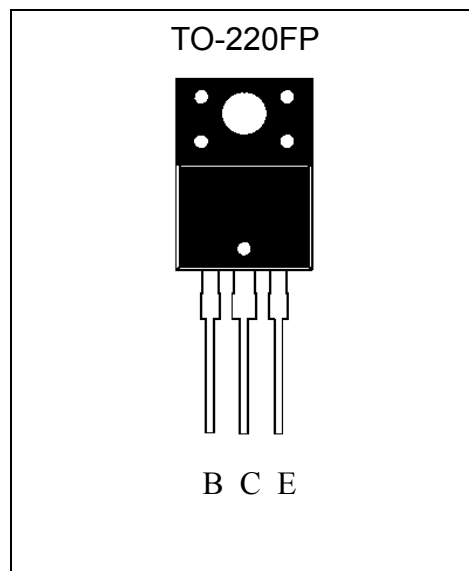
# BTA1210FP

**Description**

The BTA1210FP is a PNP Darlington transistor, designed for use in general purpose amplifier and low speed switching application.

**Features**

- High  $BV_{CEO}$
- High DC current gain
- High current capability
- Monolithic construction with built-in base-emitter shunt resistors
- Pb-free package

**Equivalent Circuit****Outline**



## Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Collector-Base Voltage	V <sub>CBO</sub>	-120	V
Collector-Emitter Voltage	V <sub>CEO</sub>	-120	V
Emitter-Base Voltage	V <sub>EBO</sub>	-5	V
Collector Current (DC)	I <sub>C</sub>	-10	A
Collector Current (Pulse)	I <sub>CP</sub>	-15 (Note )	A
Power Dissipation	Pd(T <sub>A</sub> =25°C)	2	W
	Pd(T <sub>C</sub> =25°C)	60	W
Thermal Resistance, Junction to Ambient	R <sub>θJA</sub>	62.5	°C/W
Thermal Resistance, Junction to Case	R <sub>θJC</sub>	2.08	°C/W
Junction Temperature	T <sub>j</sub>	150	°C
Storage Temperature	T <sub>stg</sub>	-55~+150	°C

Note : Single Pulse Pw ≤ 350μs, Duty ≤ 2%.

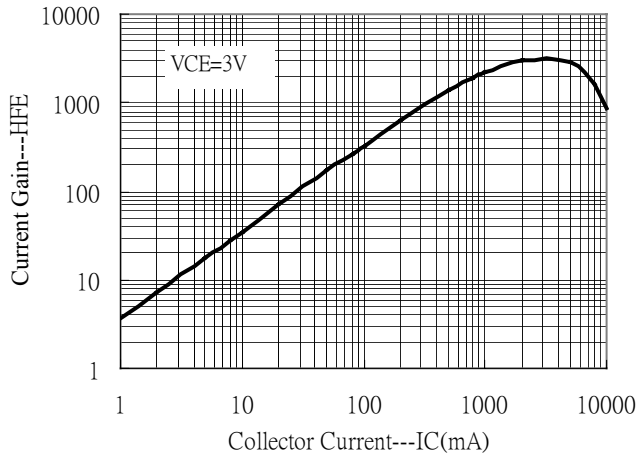
## Characteristics (Ta=25°C)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
BV <sub>CEO</sub>	-120	-	-	V	I <sub>C</sub> =-1mA, I <sub>B</sub> =0
BV <sub>CBO</sub>	-120	-	-	V	I <sub>C</sub> =-100μA, I <sub>E</sub> =0
BV <sub>EBO</sub>	-5	-	-	V	I <sub>E</sub> =-1mA, I <sub>C</sub> =0
I <sub>CBO</sub>	-	-	-200	μA	V <sub>CB</sub> =-120V, I <sub>E</sub> =0
I <sub>CEO</sub>	-	-	-200	μA	V <sub>CE</sub> =-120V, I <sub>B</sub> =0
I <sub>EBO</sub>	-	-	-2	mA	V <sub>EB</sub> =-5V, I <sub>C</sub> =0
*V <sub>CE(sat)</sub> 1	-	-	-2	V	I <sub>C</sub> =-4A, I <sub>B</sub> =-16mA
*V <sub>CE(sat)</sub> 2	-	-	-4	V	I <sub>C</sub> =-8A, I <sub>B</sub> =-80mA
*V <sub>BE(sat)</sub>	-	-	-4.5	V	I <sub>C</sub> =-8A, I <sub>B</sub> =-80mA
*V <sub>BE(on)</sub>			-2.8	V	V <sub>CE</sub> =-4V, I <sub>C</sub> =-4A
*h <sub>FE1</sub>	1	-	12	K	V <sub>CE</sub> =-4V, I <sub>C</sub> =-4A
*h <sub>FE2</sub>	100	-	-	-	V <sub>CE</sub> =-4V, I <sub>C</sub> =-8A
Cob	-		300	pF	V <sub>CB</sub> =-10V, I <sub>E</sub> =0A, f=1MHz

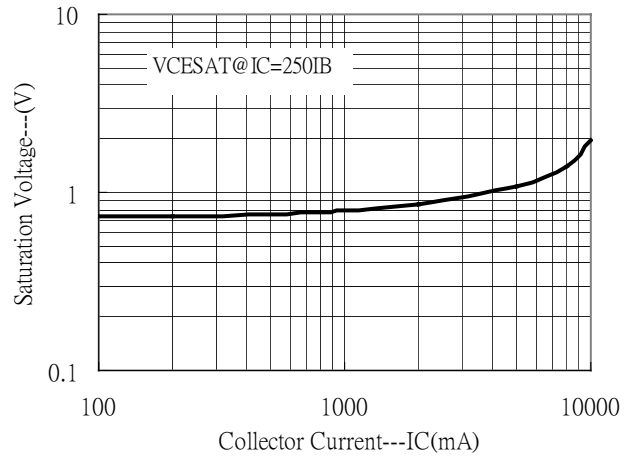
\*Pulse Test : Pulse Width ≤ 380μs, Duty Cycle ≤ 2%

## Characteristic Curves

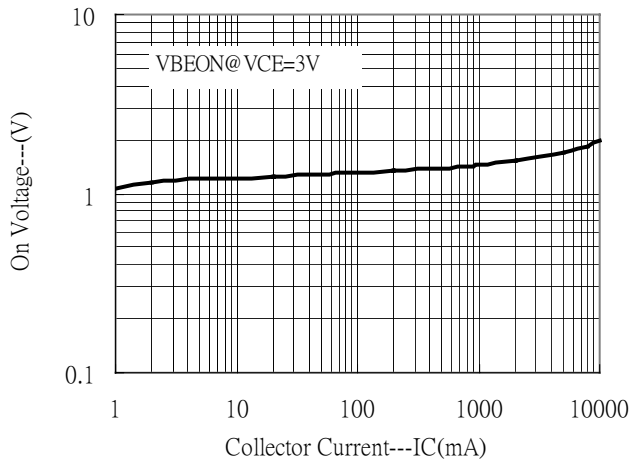
Current Gain vs Collector Current



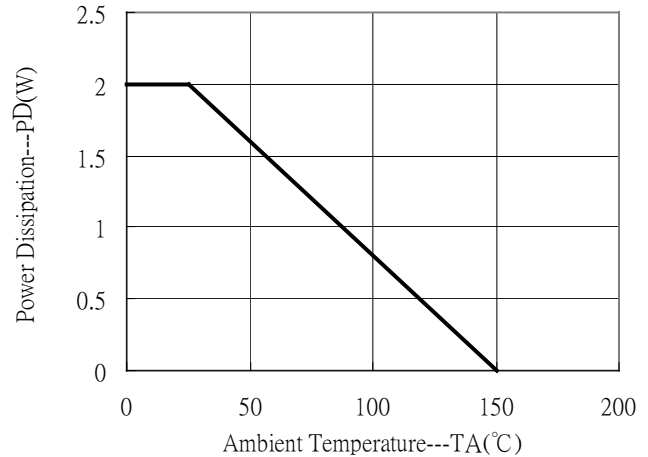
Saturation Voltage vs Collector Current



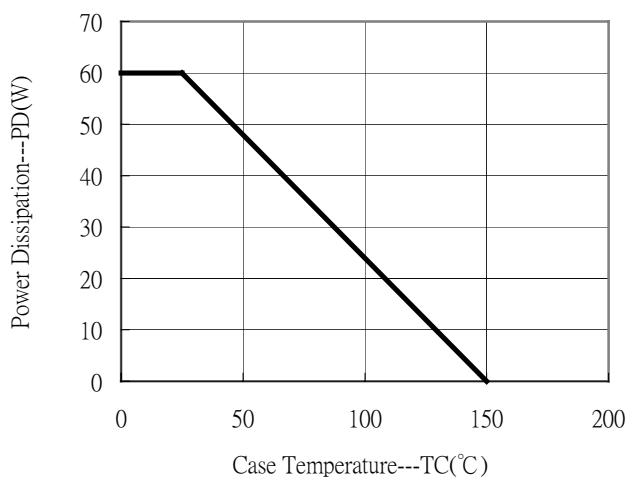
On voltage vs Collector Current



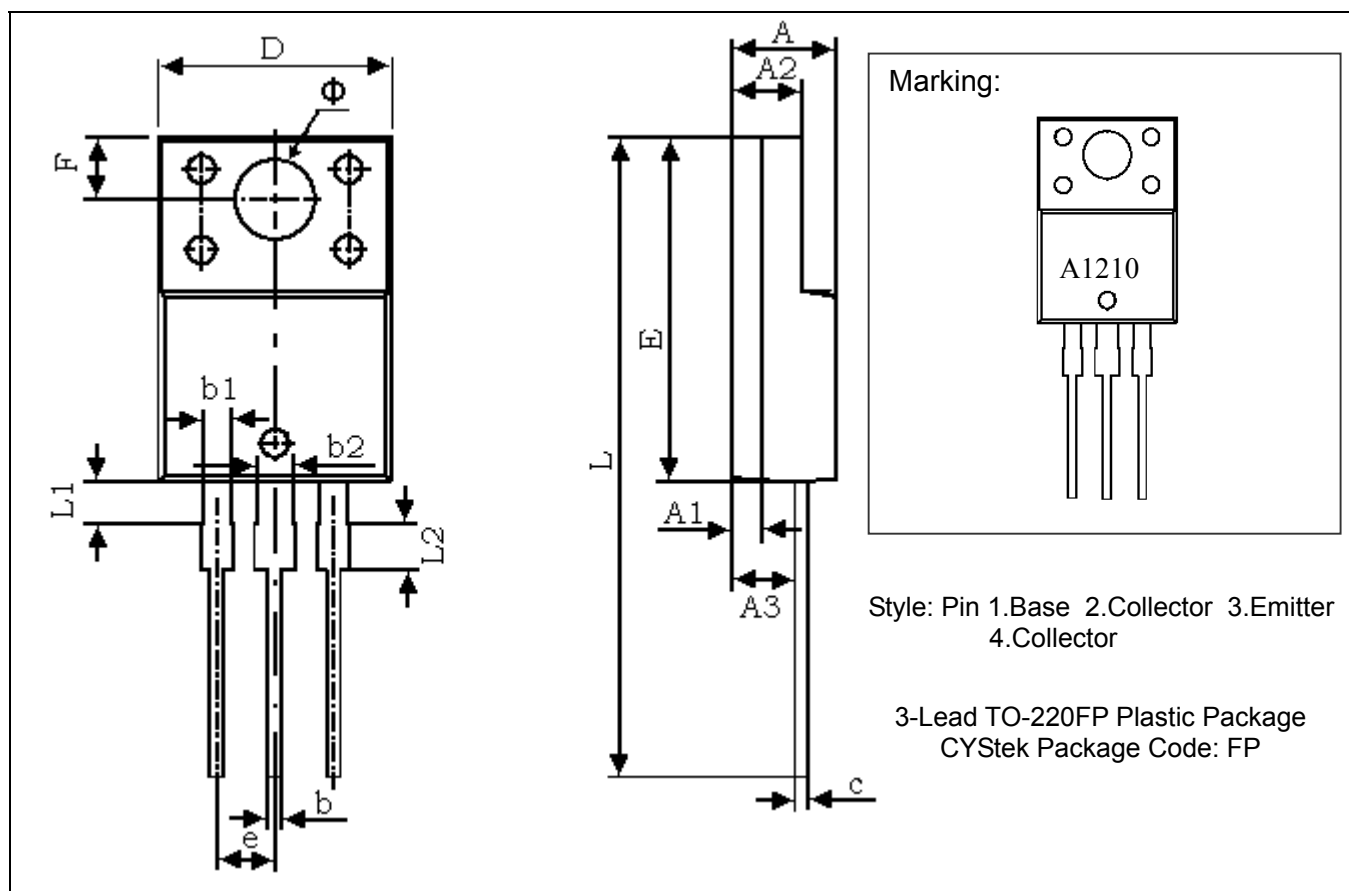
Power Derating Curve



Power Derating Curve



## TO-220FP Dimension



\*: Typical

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.169	0.185	4.300	4.700	D	0.392	0.408	9.960	10.360
A1	0.051 REF		1.300 REF		E	0.583	0.598	14.800	15.200
A2	0.110	0.126	2.800	3.200	e	0.100 TYP		2.540 TYP	
A3	0.098	0.114	2.500	2.900	F	0.106 REF		2.700 REF	
b	0.020	0.030	0.500	0.750	$\Phi$	0.138 REF		3.500 REF	
b1	0.043	0.053	1.100	1.350	L	1.102	1.118	28.000	28.400
b2	0.059	0.069	1.500	1.750	L1	0.067	0.075	1.700	1.900
c	0.020	0.030	0.500	0.750	L2	0.075	0.083	1.900	2.100

Notes: 1.Controlling dimension: millimeters.

2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.

3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

### Material:

- Lead: 42 Alloy ; solder plating
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0

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