

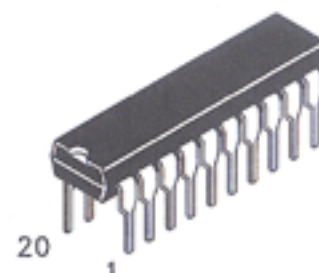
DV74AC373 Available Q2, 1995

Octal 3-State Non-Inverting Transparent Latch

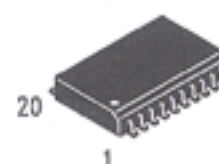
The DV74AC/74ACT373 circuits consist of eight latches with 3-state outputs for bus organized system applications. The flip-flops appear transparent to the data (data changes asynchronously). Data is latched at the falling edge of Latch Enable. The high-impedance state and increased high-logic level drive provide these registers with the capability of being connected directly to and driving the bus lines in a bus-organized system without need for interface or pull-up components. They are particularly suited for buffer registers, I/O ports, bi-directional bus drivers, and working registers.

- Advanced very high speed CMOS
- Outputs source/sink 24 mA
- Transmission line driving 50 ohms
- ACT has TTL compatible inputs
- Operation from 2 to 6 volts guaranteed
- DC & AC Parameters guaranteed over -40 to $+85^{\circ}\text{C}$

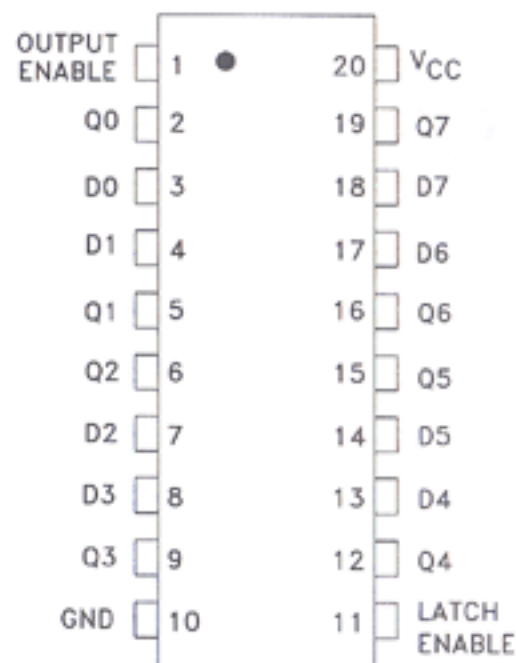
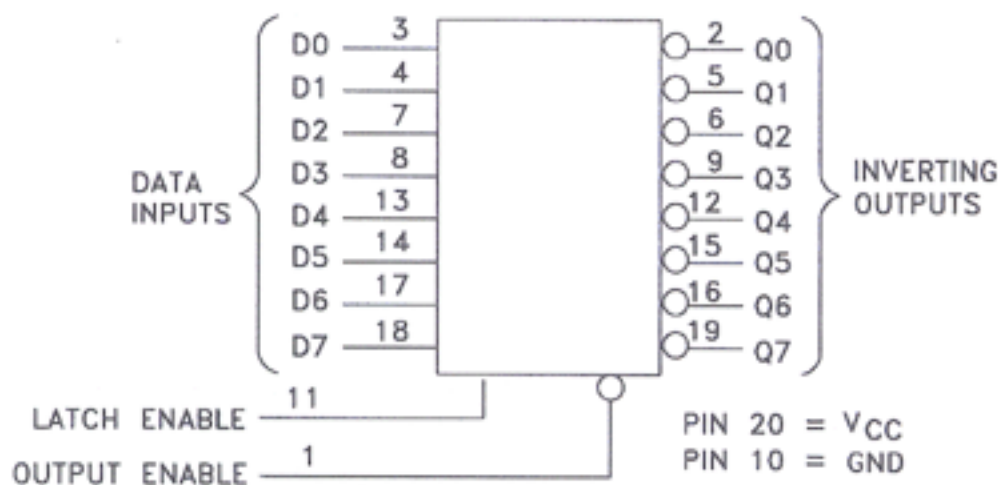
DV74AC373
DV74ACT373



N Suffix
Plastic DIP
AVG-005 Case



D Suffix
Plastic SOP
AVG-006 Case



TRUTH TABLE

Inputs			Outputs
Output Enable	Latch Enable	Dn	Qn
H	X	X	Z
L	H	L	L
L	H	H	H
L	L	X	Q0

H=HIGH Voltage Level

L=LOW Voltage Level

Z=High Impedance

X=Immaterial

Q0=Previous Q0 before LOW-to-HIGH Transition of Clock

ABSOLUTE MAXIMUM RATINGS

Maximum ratings are those values beyond which damage to the device may occur.

Symbol	Parameter	AC373, ACT373	Unit
V _{CC}	DC Supply Voltage (Referenced to GND)	– 0.5 to +7.0	V
V _{IN}	DC Input Voltage (Referenced to GND)	– 0.5 to V _{CC} +0.5	V
V _{OUT}	DC Output Voltage (Referenced to GND)	– 0.5 to V _{CC} +0.5	V
I _{IN}	DC Input Current, per Pin	± 20	mA
I _{OUT}	DC Output Sink/Source Current, per Pin	± 50	mA
I _{CC}	DC V _{CC} or GND Current per Output Pin	± 50	mA
T _{STG}	Storage Temperature	– 65 to +150	°C

GUARANTEED OPERATING CONDITIONS

Symbol	Parameter	Min	Typ	Max	Unit
V _{CC}	Supply Voltage	'AC	2.0	5.0	V
		'ACT	4.5	5.0	
V _{IN} , V _{OUT}	DC Input Voltage, Output Voltage, (Ref. to GND)	0		V _{CC}	V
t _r , t _f	Input Rise and Fall Time (Note 1) AC Devices	V _{CC} @ 3.0 V		150	ns/V
		V _{CC} @ 4.5 V		40	ns/V
		V _{CC} @ 5.5 V		25	ns/V
t _r , t _f	Input Rise and Fall Time (Note 2) ACT Devices	V _{CC} @ 4.5 V		10	ns/V
		V _{CC} @ 5.5 V		8.0	ns/V
T _A	Operating Ambient Temperature Range	–40		85	°C
C _{IN}	Input Capacitance V _{CC} = 5.0 V	V _{CC} = 5.0 V	4.5		pF
C _{PD}	Power Dissipation Capacitance	V _{CC} = 5.0 V	30		pF

1. V_{IN} from 30% to 70% V_{CC}

2. V_{IN} from 0.8 to 2.0 V

AC —373

DC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Conditions	V _{CC} (V)	AC373			Unit
				TA = +25°C		TA = −40 to +85°C	
				Typ	Guaranteed Limits		
V _{IH}	Minimum High Level Input Voltage	V _{OUT} = 0.1V or V _{CC} − 0.1 V	3.0	1.5	2.1	2.1	V
			4.5	2.25	3.15	3.15	
			5.5	2.75	3.85	3.85	
V _{IL}	Maximum Low Level Input Voltage	V _{OUT} = 0.1V or V _{CC} − 0.1 V	3.0	1.5	0.9	0.9	V
			4.5	2.25	1.35	1.35	
			5.5	2.75	1.65	1.65	
V _{OH}	Minimum High Level Output Voltage	I _{OUT} = −50 μA	3.0	2.99	2.9	2.9	V
			4.5	4.49	4.4	4.4	
			5.5	5.49	5.4	5.4	
		V _{IN} = V _{IL} or V _{IH}					V
	I _{OH} −12mA	3.0		2.56	2.46		
	−24mA	4.5		3.86	3.76		
		−24 mA	5.5		4.86	4.76	

Symbol	Parameter	Conditions	V _{CC} (V)	AC373			Unit
				T _A = +25°C		T _A = -40 to +85°C	
				Typ	Guaranteed Limits		
V _{OL}	Maximum Low Level Output Voltage	I _{OUT} = 50 μA	3.0	0.002	0.1	0.1	V
			4.5	0.001	0.1	0.1	
			5.5	0.001	0.1	0.1	
		V _{IN} = V _{IL} or V _{IH} 12mA I _{OL} 24mA 24 mA	3.0		0.36	0.44	V
			4.5		0.36	0.44	
			5.5		0.36	0.44	
I _{IN}	Maximum Input Leakage Current	V _{IN} =V _{CC} , GND	5.5		±0.1	±1.0	μA
I _{OZ}	Maximum 3-State Current	V _{IN} (OE)=V _{IL} , V _{IH} V _{IN} =V _{CC} , GND V _{OUT} =V _{CC} , GND	5.5		±0.5	±5.0	μA
I _{CC}	Maximum Quiescent Supply Current	V _{IN} = V _{CC} or GND	5.5		8.0	80	μA

AC CHARACTERISTICS

Symbol	Parameter (C _L = 50 pF)	V _{CC} (V) ±10%	AC373				Unit
			T _A = +25°C		T _A = - 40°C to +85°C		
			Min	Max	Min	Max	
t _{PLH}	Propagation Delay D _n to Q _n	3.3	2.5	13.0	2.0	15.0	ns
t _{PHL}		5.0	2.5	10.0	2.0	11.5	
		3.3	2.5	12.0	2.0	14.0	
		5.0	2.5	9.5	2.0	11.0	
t _{PLH}	Propagation Delay Latch Enable to Q _n	3.3	2.5	13.0	2.0	15.0	ns
t _{PHL}		5.0	2.5	9.5	2.0	11.0	
		3.3	2.5	12.0	2.0	14.0	
		5.0	2.5	8.5	2.0	10.0	
t _{PZH}	Output Enable Time	3.3	2.5	11.0	2.0	12.0	ns
t _{PZL}		5.0	2.5	9.0	2.0	10.0	
		3.3	2.5	11.0	2.0	12.5	
		5.0	2.5	8.5	2.0	9.5	
t _{PHZ}	Output Disable Time	3.3	2.5	12.5	2.0	13.5	ns
t _{PLZ}		5.0	2.5	11.0	2.0	12.0	
		3.3	2.5	9.5	2.0	10.5	
		5.0	2.5	8.0	2.0	9.0	

AC OPERATING REQUIREMENTS

Symbol	Parameter (C _L = 50 pF)	V _{CC} (V) ±10%	AC373		Unit
			T _A = +25°C	T _A = -40°C to +85°C	
t _s	Setup Time, HIGH or LOW, D _n to Latch Enable	3.3	3.5	4.0	ns
		5.0	3.0	3.5	
t _h	Hold Time, HIGH or LOW, D _n to Clock	3.3	2.0	2.0	ns
		5.0	2.0	2.0	
t _w	Clock Pulse Width, HIGH or LOW	3.3	6.0	7.0	ns
		5.0	4.0	5.0	

DC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Conditions	V _{CC} (V)	ACT373			Unit
				T _A = +25°C		T _A = -40 to +85°C	
				Typ	Guaranteed Limits		
V _{IH}	Minimum High Level Input Voltage	V _{OUT} = 0.1V or V _{CC} - 0.1 V	4.5 5.5	1.5 1.5	2.0 2.0	2.0 2.0	V
V _{IL}	Maximum Low Level Input Voltage	V _{OUT} = 0.1V or V _{CC} - 0.1 V	4.5 5.5	1.5 1.5	0.8 0.8	0.8 0.8	V
V _{OH}	Minimum High Level Output Voltage	I _{OUT} = -50 μA	4.5 5.5	4.49 5.49	4.4 5.4	4.4 5.4	V
		V _{IN} = V _{IL} or V _{IH} I _{OH} -24mA -24 mA	4.5 5.5		3.86 4.86	3.76 4.76	V
V _{OL}	Maximum Low Level Output Voltage	I _{OUT} = 50 μA	4.5 5.5	0.001 0.001	0.1 0.1	0.1 0.1	V
		V _{IN} = V _{IL} or V _{IH} I _{OL} 24mA 24 mA	4.5 5.5		0.36 0.36	0.44 0.44	V
I _{IN}	Maximum Input Leakage Current	V _{IN} =V _{CC} , GND	5.5		±0.1	±1.0	μA
I _{OZ}	Maximum 3-State Current	V _{IN} (OE)=V _{IL} , V _{IH} V _{IN} =V _{CC} , GND V _{OUT} =V _{CC} , GND	5.5		±0.6	±6.0	μA
ΔI _{CC} T	Additional Max I _{CC} /Input	V _{IN} =V _{CC} - 2.1 V	5.5	0.6		1.5	mA
I _{CC}	Maximum Quiescent Supply Current	V _{IN} = V _{CC} or GND	5.5		8.0	80	mA

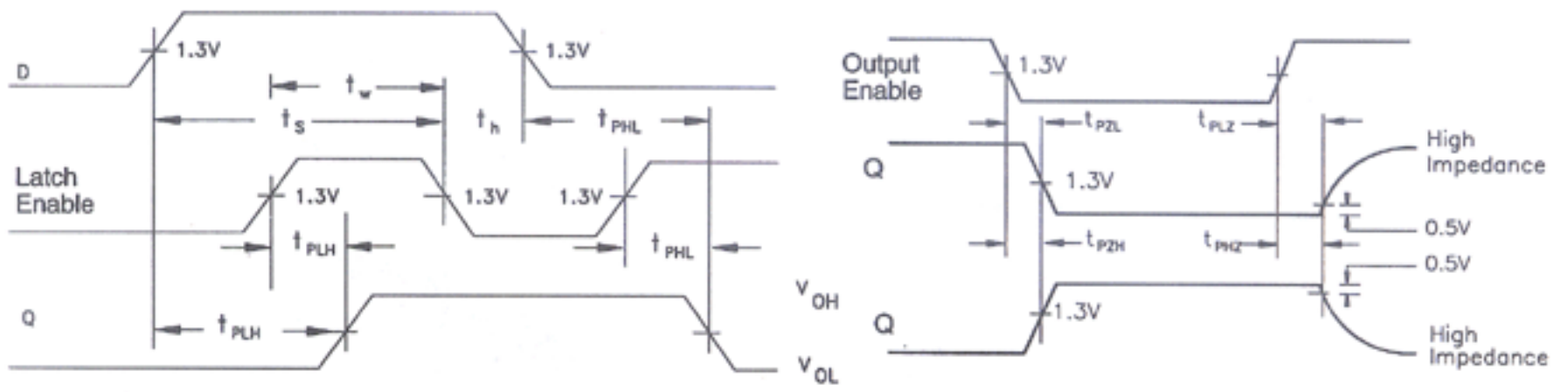
AC CHARACTERISTICS

Symbol	Parameter (C _L = 50 pF)	V _{CC} (V) ±10%	ACT373				Unit
			T _A = +25°C		T _A = - 40°C to +85°C		
			Min	Max	Min	Max	
t _{PLH}	Propogation Delay	5.0	2.5	10.5	2.0	12.0	ns
t _{PHL}	Dn to Q _n	5.0	2.5	10.5	2.0	12.0	
t _{PLH}	Propogation Delay	5.0	3.0	10.5	2.5	12.0	ns
t _{PHL}	Latch Enable to Q _n	5.0	2.5	9.5	2.0	10.5	
t _{PZH}	Output Enable Time	5.0	2.0	10.0	1.5	11.0	ns
t _{PZL}		5.0	1.5	9.5	1.5	10.5	
t _{PHZ}	Output Disable Time	5.0	2.5	11.0	1.5	12.5	ns
t _{PLZ}		5.0	1.5	8.5	1.0	9.5	

AC OPERATING REQUIREMENTS

Symbol	Parameter (C _L = 50 pF)	V _{CC} (V) ±10%	ACT373		Unit
			T _A = +25°C	T _A = − 40°C to +85°C	
			Guaranteed Minimum		
t _s	Setup Time, HIGH or LOW, Dn to LE	5.0	3.5	3.5	ns
t _h	Hold Time, HIGH or LOW, Dn to LE	5.0	0	0	ns
t _w	LE Pulse Width, HIGH or LOW	5.0	3.5	4.0	ns

SWITCHING WAVEFORMS



Input and output threshold voltage:
 $V_T = 50\% V_{CC}$ for AC; 1.5V for ACT
 $V_H = V_{CC}$ for AC, 3V for ACT